

WHAT IS CLAIMED IS:

1. An image processing device for a digital display, the device processing image signals inputted from a display card (VGA card) and comprising:
 - a peripheral circuit, which is coupled to the display card, for transmitting the image signals, wherein the peripheral circuit has a first ground; and
 - an analog front end (AFE) device, which is coupled to the peripheral circuit, for converting the image signals, wherein the AFE device has a plurality of converters for converting the image signals, and each of the converters shares a second ground that is electrically connected to the first ground.
2. The image processing device according to claim 1, wherein the image signals comprise a red signal, a green signal and a blue signal, and the converters comprise a red converter, a green converter and a blue converter for respectively converting the red, green and blue signals from analog ones into digital ones.
3. The image processing device according to claim 2, wherein the digital display is a liquid crystal display (LCD).

4. An image processing device for a liquid crystal display, the image processing device being disposed in a LCD controller, the LCD controller having a plurality of pins, through which the LCD controller is coupled to a peripheral circuit, the image processing device

5 comprising:

an analog front end (AFE) device for receiving and image-processing analog image signals outputted from the peripheral circuit, the peripheral circuit having a first ground and the AFE device having a second ground, wherein the second ground is electrically connected to the first ground via one of the pins.

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5. The image processing device according to claim 4, wherein the image signals comprise a red signal, a green signal and a blue signal, the AFE device comprise a red converter, a green converter and a blue converter for respectively converting the red, green and blue signals from analog ones into digital ones, and each of the red, green and blue converters has a second ground electrically connected to the first ground.

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6. The image processing device according to claim 5, wherein each of the second ground is electrically connected to the first ground via one of

the pins.

7. The image processing device according to claim 5, wherein the second grounds are electrically connected to the first ground via the same one of the pins.

5 8. The image processing device according to claim 4, wherein the peripheral circuit is disposed on a printed circuit board.

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